

BILL HUNG

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EDUCATION

Electrical Engineering and Computer Science Major, Bachelor of Science, Senior Standing

University of California, Berkeley

Graduating May 2006

Upper Division GPA 3.86 Overall GPA 3.93 (166.5 Quarter Units)

EXPERIENCE

President and Founder of Science and Engineering Association – Fall 2003

Invited speakers from Lockheed Martin Missiles and Space Operations, Stanford Linear Accelerator Center (SLAC), and the Intel Museum to give seminars for more than 100 members and 10 officers. Increased the funding for the Association by 10 times.

Berkeley Sensor and Actuator Center Research Assistant, UC Berkeley – Fall 2005

Developed the hardware and software of a portable Raman Medical Imaging System for the Intel Corporation. The design, with the laser, is 15 times smaller than a commercial model.

Tohoku University Nano-Spin Center Undergraduate Researcher, Japan – Spring 2005

Tested the quantum computer model with superconductor quantum theories. Handled high temperature superconductor at 0.04 Kelvin. Changed 100 liters of helium and nitrogen gas every week. Submitted final report following IEEE LaTeX format.

Full Scholarship Exchange Student in Japan Engineering Program, Japan – Summer 2005

Participated in Quantum Computing Research. Received a 4.0 GPA in Japan. Presented verbally and submitted written reports in Japanese.

Mathematics, Physics, Chemistry, and Circuit Analysis Tutor, Internship in De Anza College – Spring 2004

Help 5 under-achieving students to achieve their academic successes. A paid internship that lasted for 1 year.

Web Designer – Present

Experienced with PHP, SQL, CGI, and HTML. Developed websites for college club, private company, and my online profile at <http://www.billhung.net/career/>.

PROJECTS

Network Broadcasting Digital Music Player on Field Programmable Gate Array (FPGA) – Spring 2005

Designed a circuit using Verilog, which transformed music data stream from the network to the speaker connected to the FPGA board. Interfaced with the audio AC 97 chip and the Ethernet network ports.

Analog Audio Equalizer Circuit – Summer 2004

Built an audio equalizer circuit for a MP3 player. Optimized the output frequency range, output amplitude, and attenuation level.

High Gain Amplifier – Fall 2004

Designed an amplifier with 1000 times signal gain, 2milli-Volt to 4 milli-Volt input voltage, and 34 milli-Watt power consumption using CMOS.

Driving a DC Stepping Motor – Spring 2005

Designed a circuit to control the rotation of a DC Stepping Motor. The circuit controlled the speed and the direction of the rotation. Synchronized digital circuit that controlled the 5 Watt motor through 4 bi-polar amplifiers.

CPU Design using Hardware Description Language – Fall 2004

Built a CPU using Verilog. Verified the CPU design with MIPS assembly instructions.

COMPUTER SKILLS

Operating System: Windows XP/2000/NT/98/95/3.1, MS-DOS, Unix (Fedora, Solaris, Knoppix)

Programming: Verilog, C, C++, Java, XML, HTML, Cgi, Php, LaTeX, LabView, MIPS, SQL

Applications: Spice, Matlab, FTP, Adobe Photoshop, Visio, Frontpage, Quicken, Maya, 3D Max

ACADEMIC AWARDS

JASSO Scholarship \$4,250 with air tickets (Spring 2005)

Faculty Association Scholarship \$500 (Spring 2004)

Student Body 4.0 GPA Scholarship \$500 (Fall 2003)

Carlolee Erickson Memorial Inter Club Council Scholarship \$500 (Fall 2003)

RELEVANT CLASSES

Digital and Analog Circuit Design

C and C++ Programming

Machine Structures

Java Programming

OTHERS

Fluent in Japanese, English, and 3 dialects of Chinese including Mandarin.

F-1 Student Visa Status. Willing to Travel.