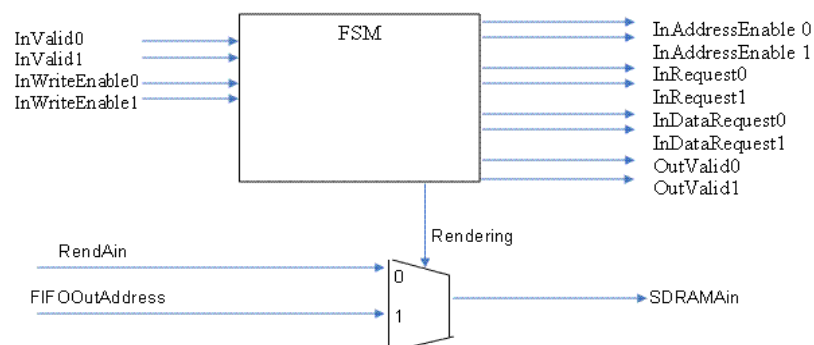


Figure 5di.1 SDRAM Arbiter Block Diagram**Figure 5di.2 SDRAM Arbiter State Diagram**